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*****
25287 Sat Feb 7 18:57:37 2015
new/usr/src/uts/armv6/os/fakebop.c
armv6: bop_panic should hexdump the stack
It's the little things that make debugging easier.
*****
1 /*
2  * This file and its contents are supplied under the terms of the
3  * Common Development and Distribution License ("CDDL"), version 1.0.
4  * You may only use this file in accordance with the terms of version
5  * 1.0 of the CDDL.
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7  * A full copy of the text of the CDDL should have accompanied this
8  * source. A copy of the CDDL is also available via the Internet at
9  * http://www.illumos.org/license/CDDL.
10 */

12 /*
13  * Copyright (c) 2014 Joyent, Inc. All rights reserved.
14  * Copyright (c) 2015 Josef 'Jeff' Sipek <jeffpc@josefsipek.net>
15 */

17 /*
18  * Just like in i86pc, we too get the joys of mimicking the SPARC boot system.
19 */

21 #include <sys/types.h>
22 #include <sys/param.h>
23 #include <sys/bootconf.h>
24 #include <sys/bootsvcs.h>
25 #include <sys/boot_console.h>
26 #include <sys/atag.h>
27 #include <sys/varargs.h>
28 #include <sys/cmn_err.h>
29 #include <sys/sysmacros.h>
30 #include <sys/system.h>
31 #include <sys/ctype.h>
32 #include <sys/bootstat.h>
33 #include <sys/privregs.h>
34 #include <sys/cpu_asm.h>
35 #include <sys/boot_mmu.h>
36 #include <sys/elf.h>
37 #include <sys/archsystem.h>
38 #endif /* ! codereview */

40 static bootops_t bootop;

42 /*
43  * Debugging help
44  */
45 static int fakebop_prop_debug = 0;
46 static int fakebop_alloc_debug = 0;
47 static int fakebop_atag_debug = 0;

49 static uint_t kbm_debug = 1;
50 #define DBG_MSG(x)    { if (kbm_debug) bcons_puts(x); bcons_puts("\n"); }
51 #define BUFFERSIZE   256
52 static char buffer[BUFFERSIZE];

54 /*
55  * fakebop memory allocations scheme
56  *
57  * It's a virtual world out there. The loader thankfully tells us all the areas
58  * that it has mapped for us and it also tells us about the page table arena --
59  * a set of addresses that have already been set aside for us. We have two
60  * different kinds of allocations to worry about:

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61  *
62  *   o Those that specify a particular vaddr
63  *   o Those that do not specify a particular vaddr
64  *
65  * Those that do not specify a particular vaddr will come out of our scratch
66  * space which is a fixed size arena of 16 MB (FAKEBOP_ALLOC_SIZE) that we set
67  * aside at the beginning of the allocator. If we end up running out of that
68  * then we'll go ahead and figure out a slightly larger area to worry about.
69  *
70  * Now, for those that do specify a particular vaddr we'll allocate more
71  * physical address space for it. The loader set aside enough L2 page tables for
72  * us that we'll go ahead and use the next 4k aligned address.
73  */
74 #define FAKEBOP_ALLOC_SIZE    (16 * 1024 * 1024)

76 static size_t bop_alloc_scratch_size;
77 static uintptr_t bop_alloc_scratch_next;      /* Next scratch address */
78 static uintptr_t bop_alloc_scratch_last;     /* Last scratch address */

80 static uintptr_t bop_alloc_pnext;            /* Next paddr */
81 static uintptr_t bop_alloc_plast;           /* cross this paddr and panic */

83 #define BI_HAS_RAMDISK    0x1

85 /*
86  * TODO Generalize this
87  * This is the set of information tha we want to gather from the various atag
88  * headers. This is simple and naive and will need to evolve as we have
89  * additional boards beyond just the RPi.
90  */
91 typedef struct bootinfo {
92     uint_t      bi_flags;
93     char        *bi_cmdline;
94     uint32_t    bi_ramdisk;
95     uint32_t    bi_ramsize;
96 } bootinfo_t;

98 static bootinfo_t bootinfo;    /* Simple set of boot information */

100 static struct boot_syscalls bop_syp = {
101     bcons_getchar,
102     bcons_putchar,
103     bcons_ischar,
104 };

106 /*
107  * stuff to store/report/manipulate boot property settings.
108  */
109 typedef struct bootprop {
110     struct bootprop *bp_next;
111     char *bp_name;
112     uint_t bp_vlen;
113     char *bp_value;
114 } bootprop_t;

116 static bootprop_t *bprops = NULL;

118 static void
119 hexdump_stack()
120 {
121     extern char t0stack[];

123     uint8_t *start = (uint8_t *)t0stack;
124     uint8_t *end = start + DEFAULTSTKSZ;
125     uint8_t *ptr;
126     int i;

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128     bop_printf(NULL, "stack (fp = %x):\n", getfp());
130     ptr = (uint8_t *) (getfp() & ~0xf);
131     if (ptr <= start || ptr >= end)
132         ptr = start;
134     while (ptr < end) {
135         uint32_t *tmp = (uint32_t *) ptr;
137         bop_printf(NULL, "%p: %08x %08x %08x %08x\n", ptr,
138             tmp[0], tmp[1], tmp[2], tmp[3]);
140         ptr += 16;
141     }
143 }
145 #endif /* ! codereview */
146 void
147 bop_panic(const char *msg)
148 {
149     bop_printf(NULL, "ARM bop_panic:\n%s\n", msg);
151     hexdump_stack();
153     bop_printf(NULL, "Spinning Forever...", msg);
154     bop_printf(NULL, "ARM bop_panic:\n%s\nSpinning Forever...", msg);
155     for (;;)
156     ;
156 }
unchanged_portion_omitted
```